

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10573733
Filing Date	2006-03-28
First Named Inventor	Leenaerts et al.
Art Unit	2816
Examiner Name	Cox, Cassandra F
Attorney Docket Number	NL031188US1

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	1	KRISHNAPURA N ET AL., "A 5.3-GHz Programmable Divider for Hiperlan in 0.25-MUM CMOS," IEEE Journal of Solid State Circuits, IEEE Inc., New York, US, vol. 35, no. 7, July 2000.	<input type="checkbox"/>

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- See attached certification statement.
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
 None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/mark a. wilson/	Date (YYYY-MM-DD)	2008-03-31
Name/Print	Mark A. Wilson	Registration Number	43994

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